

The Faculty Members and the Student Members of ECE department of

**SRI VIDYA COLLEGE OF ENGINEERING & TECHNOLOGY,
VIRUDHUNAGAR**



Cordially invite you to the inaugural ceremony of

Microprocessor Debugging
In association with ISTE

on 13th February 2019 at 09.30 a.m
in the ECE Seminar Hall



in the presence of

Dr. S.SANKARALINGAM

Principal, SVCET, Virudhunagar.

Mr. P.BALAGANESH

HOD/ECE, SVCET, Virudhunagar.



Round 1 Prelims (Time limit - 25 Minutes)

This round consists of a quiz which will be based on microprocessor and microcontroller. Questionnaire will contain 20 questions

Round 2 Final round (Time limit - 35 Minutes)

Selected teams will proceed to final round. In this round teams are given 3 questions out of which they have to attempt any 2 programs to be debugging in microprocessor and microcontroller.

Coordinator

Ms.M.Poornalakshmi

Assistant Professor/ECE