

WHO CAN ATTEND

U.G/P.G Students having interest in developing their skills in Low Power VLSI.

REGISTRATION DETAILS

UG/PG Students : **RS.300/-**
SPOT REGISTRATION AVAILABLE

IMPORTANT DATES

Intimation of Participation : 18th February 2019
Confirmation by Participants :20th February 2019

Demand Draft should be taken in favor of “The Principal, Sri Vidya College of Engineering and Technology”, Payable at Virudhunagar.

Note: The registration form must be submitted along with DD to the address for communication.

The copy of DD and Registration form may be sent to vlsilow@gmail.com

ADDRESS FOR COMMUNICATION

Mr. P.BALAGANESH
Head of the Department/ECE
Sri Vidya College of Engineering &
Technology Virudhunagar – 626001.
Mobile: 9952148148, 9003535785
Mail id: svcet.hodec@gmail.com

COORDINATORS

Mrs. G.UTHRADEVI, M.E
Assistant Professor/ECE

ORGANIZING COMMITTEE

CHIEF PATRON

Er. R.THIRUVENGADA RAMANUJA DOSS,B.E
Chairman, SVCET, Virudhunagar.

Er. T.VENKATESH, B.E
Vice-Chairman, SVCET, Virudhunagar.

Er. R.PONBADI, F.I.E.,
Chairman, Institution of Engineers (INDIA),
Madurai Local Center.

PATRON

Dr. S.SANKARALINGAM.
Principal, SVCET, Virudhunagar.

RESOURCE PERSON

Dr.A.Kaleel Rahuman ,
Associate Professor/ECE,
PSNA College of Engg & Tech,
Dindigul.

Mr.V.Vinoth Kumar B.E
Project Engineer,
Pantech ProEd Private Limited,
Madurai.

CONVENER

Mr. P.BALAGANESH, M.E
Head of the Department/ECE

VENUE

Seminar Hall @ Department of ECE

ONE DAY NATIONAL LEVEL SEMINAR ON RECENT TRENDS IN LOW POWER VLSI DESIGN

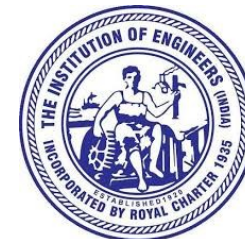
21st February 2019

Organized by



**Department of Electronics and Communication
Sri Vidya College of Engineering and
Technology, Virudhunagar-626001.
Website : www.srividyaeengg.ac.in**

**in
Association with**



ABOUT THE COLLEGE

Sri Vidya College of Engineering and Technology approved by AICTE and affiliated to Anna University, Chennai was established in the year 2008 for the upliftment of urban and rural youth by the philanthropic vision of Sri Vidya Educational Trust. The college offers 5 UG programs, 4 PG programs including Master of Business Administration. The college has full-fledged infrastructure facility in all departments. The Institution is known for strict academic discipline standards and is one of the best run institutions in this southern part of Tamil Nadu.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering (ECE) was established in the academic year 2008. The department is offering B.E degree in ECE and M.E degree in VLSI Design. The department has a team of highly qualified and dedicated faculty members. The department has laboratories with modern and state of the art equipments and excellent computing facilities.



ABOUT THE SEMINAR

The recent trends in the developments and advancements in the area of low power VLSI Design are surveyed in this seminar. Though Low Power is a well established domain, it has undergone lot of developments from transistor sizing, process shrinkage, voltage scaling, clock gating, etc., to adiabatic logic. This paper aims to elaborate on the recent trends in the low power design.

KEY TOPICS FOR DISCUSSION

The main objective of the one day seminar on recent trends in Low Power Design includes

- Classification of Power Consumption
- Traditional Power Reduction Techniques .
- Non-Conventional Methods of Low Power Design
- The methods to design in a Low power IC chips using VLSI tools.

RESOURCE PERSONS

- **Dr.A.Kaleel Rahuman** ,
Associate Professor/ECE,
PSNA College of Engg & Tech,
Dindigul.
- **Mr.V.Vinoth Kumar B.E**
Project Engineer,
Pantech ProEd Private Limited,
Madurai.

REGISTRATION FORM

Name :
Qualification :
Department :
Year :
Institution :
Email ID :
Mobile No :

I shall attend the seminar for entire duration if selected.

Date:

Signature of the Applicant

Signature of the Principal with Seal