



SRI VIDYA
COLLEGE OF ENGINEERING & TECHNOLOGY
VIRUDHUNAGAR – 626005



DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING

Cordially invites you for

GUEST LECTURE ON DIGITAL SIGNAL PROCESSING

11th March-2019 at 09.30 AM to 04.30 PM

Venue: SRS Lab, Academic Block.

Chief Guest

Dr. K. SUTHENDRAN

Professor – Kalasingam University.

Chief Patron

Er.R.Thiruvengada Ramanuja Doss, B.E.,

Chairman, Sri Vidya Group of Institutions

Er.T.Venkatesh, B.E.,

Vice-Chairman, Sri Vidya Group of Institutions

Patron

Dr.S.Sankaralingam, M.E., Ph.D.,

Principal, Sri Vidya College of Engineering & Technology

All are invited